Attorney's Docket No.: 042390P7794

Patent

#/3 1-15.03 Sum.

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Examiner: Lee, Eugene

Art Unit: 2815

In Re Application of:

Anand Murthy et al.

U.S. Serial No:

09/475,452

Filed: December 30, 1999)

For: A NOVEL MOS TRANSISTOR STRUCTURE AND METHOD OF **FABRICATION** 

**Assistant Commissioner for Patents** and Trademarks Washington, D.C. 20231

**RESPONSE** 

Dear Sir:

This is in response to the Office Action mailed October 1, 2002. Applicant respectfully requests the Examiner to enter the following amendments and consider the following remarks.

## FIRST CLASS CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Assistant Commissioner of Patents, Washington, D.C. 20231

Thursday, January 2, 2003

**Date of Deposit** 

Name of Person Mailing Correspondence

Signature

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